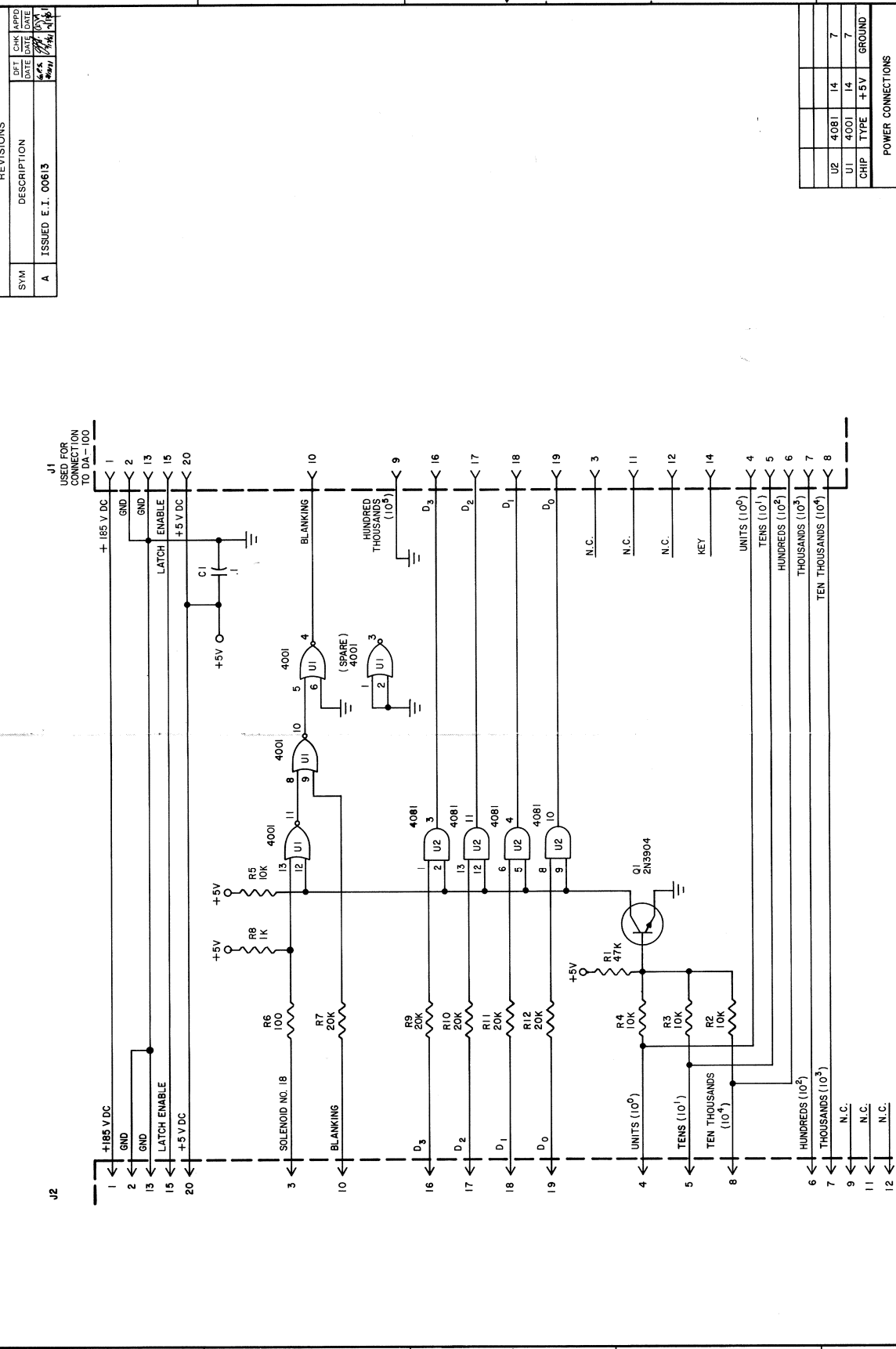


A B C D E F G



REVISIONS	
SYM	DESCRIPTION
A	ISSUED E.I. 00613

DEFI	CHK	APPD
DATE	DATE	DATE
4/2/81	2/13/81	2/13/81

U2	4081	14	7
U1	4001	14	7
CHIP	TYPE	+5V	GROUND

POWER CONNECTIONS	

DR. G.P. Shick 2/13/81

CHKD. *[Signature]* 2/13/81

APPD. *[Signature]* 2/13/81

APPD. *[Signature]* 2/13/81

MODEL BTI-100

UNIVERSAL RESEARCH LABORATORIES INC.  
A SUBSIDIARY OF STERN ELECTRONICS  
ELK GROVE VILLAGE, ILLINOIS 60007

NAME SCHEMATIC, BONUS/TIMER  
CONTROL INTERFACE P. C. BOARD

DWG PART NO. 34C-866-S

REV. A

SIZE C SHEET 1 OF 1

5 4 3 2 1